

2013

APRIL
VOLUME 3
ISSUE 1

ECE NEWS



Department of Electronics & Communication Engineering
Jyothi Engineering College, Cheruthuruthy, Thrissur - 679531

Vision

Create eminent and ethical leaders in the field of Electronics and Communication through quality professional education to excel in academia and industry.

Mission

- Provide theoretical and practical knowledge through quality education and life skills training to make competent graduates with social commitment.
- To impart entrepreneurial orientation and research motivation among the students through knowledge transfer with industrial, academic & research institutions.

NATIONAL LEVEL FDP ON 'DESIGN, TESTING AND LAUNCHING OF SATELLITE SYSTEMS'

Dept. of AEI, ECE & EEE jointly organized a Faculty Development Program on Design, Testing & Launching of Satellite Systems on 06th, 7th & 8th February 2013. Sri. S Gopakumar, Consultant-DIT Trivandrum, Dy. Director VSSC (Retd.) was the Chief Guest. Scientists from ISRO & VSSC handled the sessions.



The Chief Guest Sri. S Gopakumar, Consultant-DIT Trivandrum, Dy. Director VSSC (Retd.) delivering his talk on 'Indian Space Program'



Sri. V T Sadasivan Achari, Head, Spacecraft Actuator Systems Division (SASD), IISU, Trivandrum handling his session on 'Special Electrical Machines for Spacecraft Applications'



Sri. Hemachandran S, Group Head CSNG, IISU, Trivandrum handling his session on 'GPS Navigation'



Dr. Bibin John, Scientist / Engineer SD, LFCD, VSSC, delivering his Talk on 'Satellite Power Systems'

PAPER PUBLICATIONS BY FACULTY

1. Ms. Anjitha V, Asst. Professor published a paper titled "Optimal Design of Zig Zag Antenna using Non-linear Segment Length and Pitch Angle using B-Spline Modelling" in the National Conference on Technological Trends, Aug 2012.
2. Ms. Anjitha V, Asst. Professor published a paper titled "Optimal Design of Zig Zag Antenna using Non-linear Segment Length and Pitch Angle" in the International Journal on Procedia Technology, Science Direct, ELSEVIER, Oct 2012.



Ms. Anjitha V

FDPs / SEMINARS / WORKSHOPS ATTENDED BY FACULTY

1. Ms. Nishamary P, Asst. Professor attended the two day workshop on "Image Processing & Embedded System" organized by the Dept. of Electronics & Communication Engineering, College of Engineering, Thalassery under Technical Education Quality Improvement Programme (TEQIP) Phase-II during 15/03/2013 & 16/03/2013.
2. Mr. Jinesh K Jose, Asst. Professor attended the "Train the Trainer Workshop on Mobile Application Development using Android" organized by Infosys Campus Connect program at Lourdes Matha College of Science & Technology, Trivandrum from 13/02/13 to 15/02/13.
3. Ms. Bindhu K Rajan, Asst. Professor attended a short term training course on "Mathematics on Signal Processing" organized by GEC, Thrissur from 11/02/13 to 13/02/13

STUDENTS' ACHIEVEMENTS & ACTIVITIES

University Rank Holders 2012 B.Tech ECE Examination		Placement (2009-2013 Batch)	
1 st Rank	5 th Rank	Pen Group	
Mr. Naveen G	Ms. Anjali C K	Ms. Amala Manjijil	Ms. Rose Paul
WINNERS IN INTER COLLEGE TECHNICAL EVENTS <ol style="list-style-type: none"> 1. Mr. Shaheer Shan of 2010-2014 Batch won the 1st Prize in 'ZulbaSuthra (Matlabber)' organized by RECSA as a part of Brahmavega 2k13, National Level Tech Fest, held from 15/03/2013 to 16/03/2013 at Royal College of Engineering & Technology Campus, Thrissur. 2. Ms. Tracy Jose of 2010-2014 Batch won the 1st prize for her paper titled "Hand Vein Pattern Recognition" in the Paper Presentation contest held in South Indian IT Fest organized by the Institute of Management & Technology, Pottor, Thrissur, on 04/03/2013. 			
		Mr. Shaheer Shan	Ms. Tracy Jose
GATE & MAT QUALIFIED (2009-2013 BATCH)			
GATE		MAT	
Ms. Karthika Viswanath	Ms. Nisha Menon	Mr. Jones J Alapat	Mr. Nithin Jose

PARTICIPATION IN INTER COLLEGE TECHNICAL EVENTS

1. Ms. Amrita Wilson & Ms. Sherin Thomas of 2010-2014 Batch presented a paper titled 'Kerala Towards Smart Grid' in the 'AGRADEEP' organized by RECSA as a part of 'Brahmavega 2k13', National Level Tech Fest, held during 15/03/2013 & 16/03/2013 at Royal College of Engineering & Technology Campus, Thrissur.
2. Ms. Mariya Tony & Ms. Neshma A N of 2010-2014 Batch presented a paper titled 'Red Traction Human Area Network' in the 'AGRADEEP' organized by RECSA as a part of 'Brahmavega 2k13', National Level Tech Fest, held during 15/03/2013 & 16/03/2013 at Royal College of Engineering & Technology Campus, Thrissur.
3. Ms. JanyaJayan & Ms. Juliet Joju M of 2010-2014 Batch presented a paper titled 'Space Based Solar Power' in the 'AGRADEEP' organized by RECSA as a part of 'Brahmavega 2k13', National Level Tech Fest, held during 15/03/2013 & 16/03/2013 at Royal College of Engineering & Technology Campus, Thrissur.
4. Ms. Sherin Thomas of 2010-2014 Batch presented a paper titled 'James Webb Space Telescope (JWST)' in the 'AGRADEEP' organized by RECSA as a part of 'Brahmavega 2k13', National Level Tech Fest, held during 15/03/2013 & 16/03/2013 at Royal College of Engineering & Technology Campus, Thrissur.
5. Ms. Nimpha Anto & Ms. Niviya George of 2010-2014 Batch presented a paper titled 'Cardiac Pressure Monitor Using MEMS' in the 'AGRADEEP' organized by RECSA as a part of 'Brahmavega 2k13', National Level Tech Fest, held during 15/03/2013 & 16/03/2013 at Royal College of Engineering & Technology Campus, Thrissur.
6. Ms. Tracy Jose of 2010-2014 Batch presented a paper on "Hand Vein Pattern Recognition" in the South Indian IT Fest organized by the Institute of Management & Technology, Pottor, Thrissur, held on 04/03/2013.
7. Mr. UmeshUnni P of 2010-2014 Batch presented a paper on "Grid Connectivity of Renewable Energy – Issues & Solutions" in the State level Seminar competition organized by the KSEB Engineers Association with the support of IEEE Power & Energy Society Kerala Chapter held on 23/02/3013.
8. Mr. UmeshUnni P of 2010-2014 Batch participated in the Eastern Percussion contest in the Techno Cultural Fest 'Brahma 2013' held from 28/02/2013 to 02/03/2013 at AdiShankara Institute of Engineering & Technology, Kalady.
9. Mr. UmeshUnni P of 2010-2014 Batch participated in the Paper Presentation contest in the Techno Cultural Fest 'Lakshya 2013' held on 24/01/2013 to 26/01/2013 at Govt. Engineering College, Kozhikode.
10. Mr. Jinu K Thankachan of 2010-2014 Batch, Mr. Leo Paul & Mr. Mathew J Kodenkandath of 2011-2015 Batch participated in the "Microsoft Tech Days" technical events organized by Microsoft at St. Joseph College of Engineering & Technology, Pala.

ECSA ACTIVITIES

1. ECSA presented the Best Final Year Project award for 'Finger Print Based Gender Estimation' developed by Ms. Josmy Jose Akkara, Ms. Liviya Jacob, Ms. MerinRajanPallan, Ms. Neethu U & Ms. Rinu Jose of 2009-2013 Batch on 20/03/2013.
2. ECSA organized a workshop on LaTeX for 2010-2014 Batch students of ECE on 06/02/2013. Ms. Anjitha V, Asst. Professor, Dept. of ECE handled the session.

INDUSTRIAL VISIT & EDUCATIONAL TOUR

1. 59 Students from 2010-2014 ECE Batch visited FACT & KSEB, Madakketty Substation, Thrissur on 01/02/2013 along with Mr. Jinesh Jose, Asst. Professor & Ms. Sruthi Krishna K, Lecturer.
2. 2009-2013 Batch visited National Institute of Oceanography, Goa & Augmented IQ Solutions, Hyderabad as a part of their 7 Day Educational Tour to Goa & Hyderabad during 25/01/13 to 02/02/13 along with Mr. Abin Johns Raju, Asst. Professor & Ms. Nishamary P, Asst. Professor.
3. 2011-2015 ECE Batch visited Keltron Ceramics Ltd., Kuttippuram & Peekay Steels Ltd., Calicut as a part of their 2 Day Educational Tour to Calicut & Wayanad between 10/01/2013 & 12/01/2013 along with Mr. Abraham P A, Asst. Professor & Ms. Bindhu K Rajan, Asst. Professor.

SOCIAL SERVICE RELATED ACTIVITIES

1. 33 Students of 2011-2015 ECE Batch visited 'Abhayam' Old-age Home, Kulappully as a part of Social Service on 24/01/2013.
2. 48 Students of 2011-2015 ECE Batch visited Govt. Old Age Home, Ramavarmapuram, Thrissur on 31/12/2012.

Programme Educational Objectives (PEOs)

- I. Graduates shall have fundamental and advanced knowledge in electronics and communication engineering along with knowledge in mathematics, science and computing and get employed in national or international organizations or government agencies.
- II. Graduates shall have ability in analyzing, designing and creating innovative solutions which lead to a lifelong learning process or higher qualification, making them experts in their profession thus helping to solve electronics & communication engineering and social problems.
- III. Graduates shall have good organizing capability, presentation skills, communicating ability, leadership, team work and ethical practices.

Programme Outcomes (POs)

The Program Outcomes of Under-Graduate programs in Electronics & Communication Engineering are:

Students will have

- (a)** Ability to apply knowledge of mathematics, science and engineering in the field of Electronics & Communication engineering.
- (b)** Ability to design and conduct experiments, analyze and interpret data and results in Electronics & Communication engineering.
- (c)** Ability to design Electronics & Communication systems, components, or processes to meet the desired needs within realistic constraints such as health and safety, manufacturability and sustainability with economic, environmental, social, political and ethical considerations.
- (d)** Ability to work individually as well as in multidisciplinary teams as a member or as a leader to accomplish the common goal.
- (e)** Ability to identify, analyze, formulate, and solve engineering problems
- (f)** Knowledge of professional and ethical responsibility.
- (g)** Ability to communicate effectively in both verbal and written form in any area of their field to the industry, and society.
- (h)** Broad knowledge necessary to understand the impact of engineering solutions on individuals, organizations and society.
- (i)** Ability to engage in life-long learning to update their knowledge to keep pace with changes caused by dynamic nature of engineering field.
- (j)** Awareness of the contemporary issues that help to integrate advanced and sustainable solutions in the user environment.
- (k)** Ability to use the techniques, skills, and modern engineering tools, software and equipment necessary for designing, developing electronic systems.